

**ANODE SIDE** 

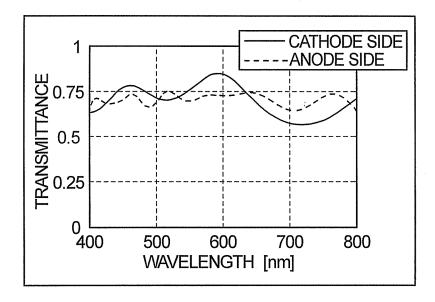
**肾**[[]**[**]. **1** 

ANODE SIDE	THICKNESS(nm)	THICKNESS(nm)   CATHODE SIDE	THICKNESS(nm)
0 LAYER(LIGHT EMITTING LAYER)	0	0 LAYER(LIGHT EMITTING LAYER)	0
1st (HTL / HIL)	33	1st LAYER(ETL / EIL)	06
2nd(ITO)	110	2ndLAYER(Ag)	∞
September 2018	100	3rdLAYER(ITO)	260
**************************************	1000	4thLAYER(SP-SiN)	100
**Sth(SiN(O)	100	5thLAYER(AIR)	0
X:0th(G1)	110	GLASS SUBSTRATE	
**************************************	100		
** Seth (SINIO)	50		
GLASS SUBSTRATE			

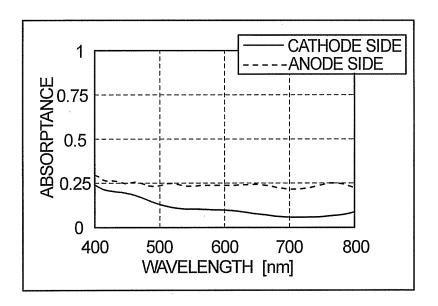
х	х	х	
$\circ$	v	V	
ヘ	$\sim$	Λ	
×	×	X	
: )	$\langle \cdot \rangle$	ĸΧ	
х	х	X	
$\sim$	•	ĸλ	
へ	Λ	А	
×	Y	V	
$^{\circ}$	$^{\circ}$	$^{\circ}$	
x	х	X	
$\circ$	v	V	
Х	Х	Л	
v	v	v	
	$^{\circ}$	$^{\circ}$	
×	×	X	
$\cdot$	ς,	ĸΧ	
х	х	х	
v	V	V	
$^{\wedge}$	Λ	Λ	
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×	х	X	
v	v	V	
ヘ	Λ	Λ	
v	v	V	
()	$\langle \cdot \rangle$	C)	
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$\circ$	♡	K.X	
х	х	N	
¥	v	V	
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LAMINATED STRUCTURE
ANODE LAYER TFT \ ITO \ EL(HIL \ HTL \ EML) \ EL(EML \ ETL \ EIL) \ Ag \ ITO \ SP-SiN CATHODE LAYER

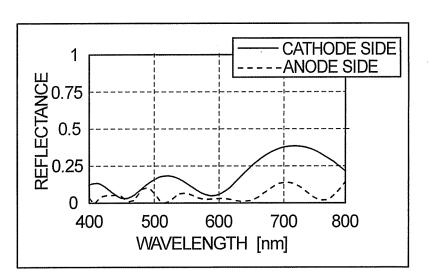
A LIGHT
EMITTING
LAYER

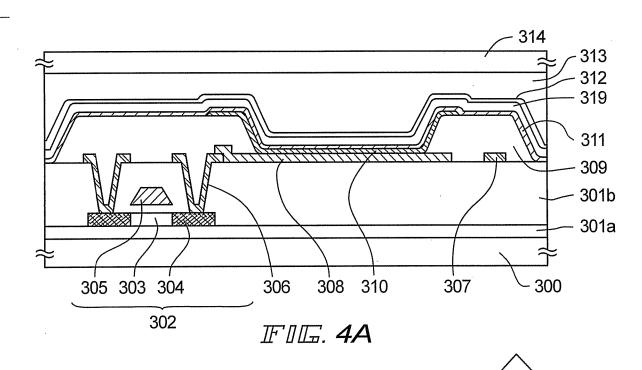


**IF** □ □ 3A



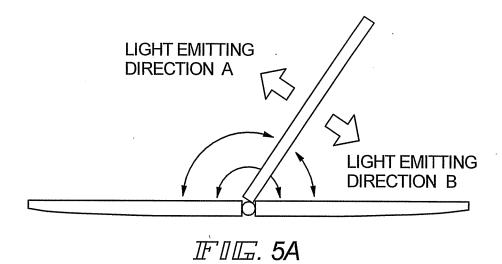
**肾**//**二**. 3B

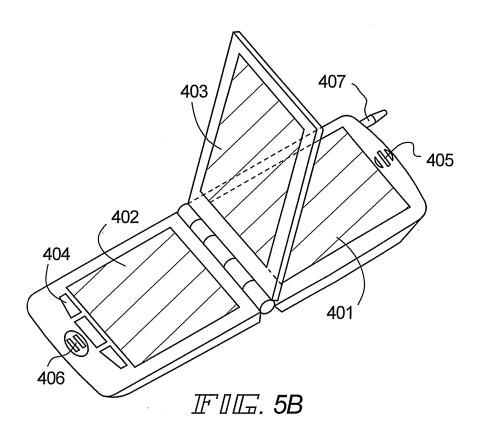


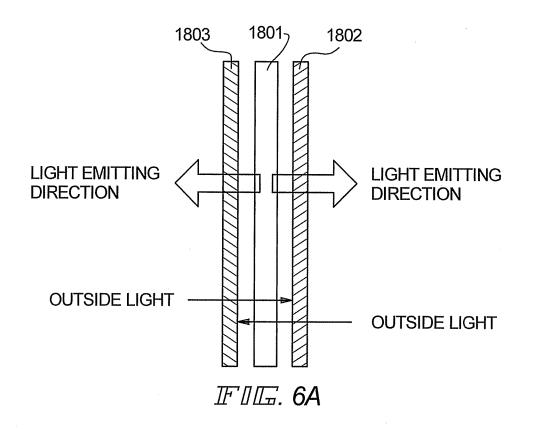


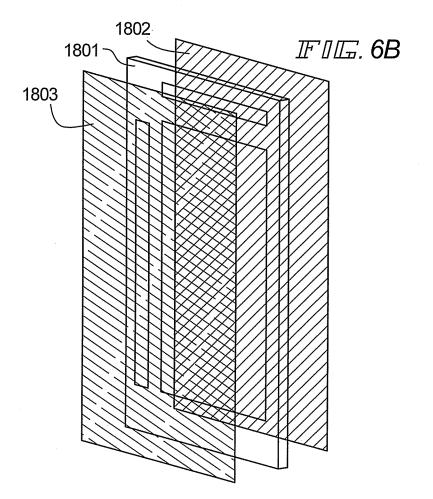
		i	
314 : SECOND SUBS	STRATE		
313 : SPACE			
312 : TRANSPARENT	PROTECTIVE FILM		
319 : TRANSPAREN	CONDUCTIVE LAYER		
311 : METAL THIN FIL	M (SECOND ELECTRODE (Ag) )		
310 : E L LAYER	EIL(BCP : Li) ETL(BCP) EML HTL(α-NPD) HIL(CuPc)		
318 : TRANSPARENT CONDUCTIVE LAYER (FIRST ELECTRODE)			
301 : INSULATING L	AYER		
300 : FIRST SUBST	RATE		

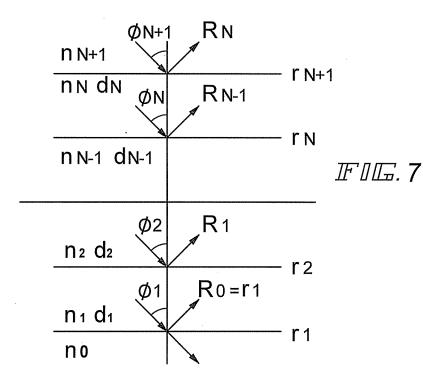
**肾**肾**二**. **4B** 

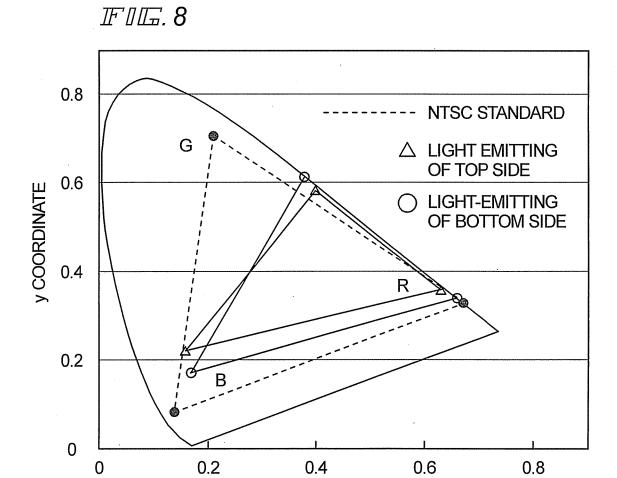












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